

**REMARKS**

Applicants appreciate the Examiner's thorough consideration provided the present application. Original claims 1-19 are present in the application. Claims 1, 10 and 16 are independent. Reconsideration of this application is respectfully requested.

**Specification Objection**

The specification has been objected to due to the presence of minor informalities. In view of the foregoing amendments, it is respectfully submitted that this objection has been addressed. Accordingly, Applicants respectfully submit that this objection have been obviated and/or rendered moot. Furthermore, Applicants also corrected other minor informalities in the specification. Applicants respectfully submit that no new matter is added. Reconsideration and withdrawal of this objection are respectfully requested.

**Claim Rejections Under 35 U.S.C. § 103**

Claims 1-19 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Applicants' admitted prior art in view of Hatori et al., U.S. Patent No. 6,156,433. This rejection is respectfully traversed.

Hatori discloses laminating the film onto a substrate so that the pattern forming layer faces the substrate; exposing the pattern forming layer from above the film through a predetermined mask; **separating and removing the film and developing the pattern forming layer to remove unexposed area**; and firing the whole substrate to adhere the

pattern forming layer in its exposed area to the substrate (see col. 5, lines 5-13). Hatori does not disclose the step 'forming a bus electrode with a pi side on portion of said transparent conductive film layer and on portion of said glass substrate, **"said pi side being located on said pattern of cavity"** as recited in claim 1, "etching said transparent conductive film and forming a cavity pattern in said transparent conductive film and forming a transparent conductive electrode, **"portion of said glass substrate being exposed from said cavity pattern"**", and "forming a bus electrode with a pi side on said glass substrate and on said transparent conductive film, **"wherein said pi side of said bus electrode being on said cavity pattern"**" as recited in claim 10, or **"wherein one pi side of said pi-type bus electrode being in said cavity and on said glass substrate"** as recited in claim 16. Hatori shows "the pattern forming layer in its exposed area to the substrate", but does not indicate that the **"bus electrode is formed on the cavity and on ITO electrode"**. Thus, the Applicants' admitted prior art in view of Hatori cannot achieve the present invention.

Accordingly, none of the references utilized by the Examiner individually or in combination teach or suggest the limitations of independent claims 1, 10 and 16 or their dependent claims. Therefore, Applicants respectfully submit that all of the claims clearly define over the teachings of the references relied on by the Examiner.

Accordingly, reconsideration and withdrawal of the rejection under 35 U.S.C. § 103 are respectfully requested.

### CONCLUSION

Since the remaining patents cited by the Examiner have not been utilized to reject the claims, but merely to show the state of the prior art, no further comments are necessary with respect thereto.

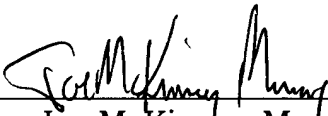
It is believed that a full and complete response has been made to the Office Action, and that as such, the Examiner is respectfully requested to send the application to Issue.

In the event there are any matters remaining in this application, the Examiner is invited to contact Joe McKinney Muncy, Registration No. 32,334 at (703) 205-8000 in the Washington, D.C. area.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

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